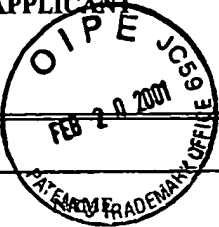


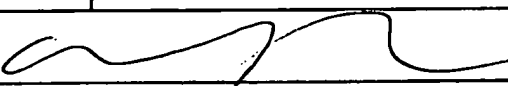
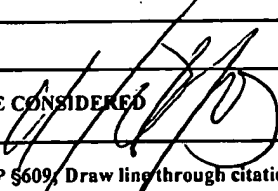
Form PTO-1449 (Modified)					Atty. Docket No. 1662-25000 (P00-3078)		Serial No. 09/740,419	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)					Applicants Andre C. SEZNEC et al.			
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE		CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	Translation YES NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
h	AA	Multiple-Block Ahead Branch Predictors, A. Sez nec et al., ASPLOS VII 10/96 MA, USA, 1996 ACM 0-89791-767-7/96/0010, Pages 116-127
h	AB	Trace Cache: a Low Latency Approach to High Bandwidth Instruction Fetching, E. Rotenberg et al., 1072-4451/96 1996 IEEE, Pages 24-34
h	AC	Increasing the Instruction Fetch Rate via Multiple Branch Prediction and a Branch Address Cache, T. Yeh et al., 1993 ACM 0-89791-600-X/93/0007/0067, Pages 67-76
h	AD	Multiple Branch and Block Prediction, S. Wallace et al., 1997 IEEE, Proceedings of the Third International Symposium on High Performance Computer Architecture, February 1-5, 1997, Pages 1-10, plus cover sheet

EXAMINER 	DATE CONSIDERED 
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Atty. Docket No.
1662-25000 (P00-3078)Serial No.
09/740,419

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Applicants
Andre C. SEZNEC et al.Filing Date
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	Translation	
							YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

2	AA	Dynamic Path-Based Branch Correlation, R. Nair, 1072-4451/95 1995 IEEE, Pages 15-23.
~	AB	Two-Level Adaptive Training Branch Prediction, Tse-Yu Yeh et al., 1991 ACM 0-89791-460-0/91/0011/0051, Pages 51-61.
~	AC	Multiple-Block Ahead Branch Predictors, A. Sez nec et al., ASPLOS VIII 10/96; 1996 ACM 0-89791-767-7/96/0010 (12 p.).

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